

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing Of Claims:**

1.-17. (Canceled)

18. (New) A motor vehicle control unit, comprising:  
a processor;  
a first interface for communicating with a functional unit of a motor vehicle; and  
at least one second interface combined with the processor in a sub-assembly.

19. (New) The motor vehicle control unit as recited in Claim 18, further comprising:  
an engine control unit.

20. (New) The motor vehicle control unit as recited in Claim 18, further comprising:  
a storage module, wherein:  
the at least one second interface accesses the storage module without participation  
of the processor.

21. (New) The motor vehicle control unit as recited in Claim 18, further comprising:  
a storage module, wherein:  
the at least one second interface accesses a code of the processor in the storage  
module for a writing purpose.

22. (New) The motor vehicle control unit as recited in Claim 18, wherein:  
the at least one second interface performs a block transfer of data.

23. (New) The motor vehicle control unit as recited in Claim 18, wherein:  
the first interface is combined with the processor and the at least one second  
interface in the sub-assembly.

24. (New) The motor vehicle control unit as recited in Claim 18, wherein:  
the sub-assembly includes a printed-circuit board.
25. (New) The motor vehicle control unit as recited in Claim 18, wherein:  
the sub-assembly includes a semiconductor chip.
26. (New) The motor vehicle control unit as recited in Claim 18, further comprising:  
a storage module for storing operating parameters of the processor, wherein:  
the storage module is able to be at least one of written on and read out via the at  
least one second interface.
27. (New) The motor vehicle control unit as recited in Claim 18, wherein:  
the at least one second interface includes a serial interface.
28. (New) The motor vehicle control unit as recited in Claim 18, wherein:  
the at least one second interface includes one of an ethernet and a FireWire  
interface.
29. (New) The motor vehicle control unit as recited in Claim 18, wherein:  
the at least one second interface includes a USB interface.
30. (New) The motor vehicle control unit as recited in Claim 18, wherein:  
the at least one second interface transmits data received from the processor via the  
first interface in an isochronous mode.
31. (New) The motor vehicle control unit as recited in Claim 18, wherein:  
the at least one second interface transmits control parameters of the processor in  
bulk mode.
32. (New) The motor vehicle control unit as recited in Claim 18, further comprising:  
a storage module, wherein:  
the at least one second interface is able to at least one of read and write to  
individual storage locations of the storage module in an interrupt mode.

33. (New) The motor vehicle control unit as recited in Claim 18, wherein:  
the at least one second interface is connected to no functional unit of a motor vehicle that is to be controlled.

34. (New) A method for communicating between a motor vehicle control unit and an external host, comprising:  
causing the external host to stipulate different USB endpoints and transmission modes for different types of data to be exchanged between the external host and the motor vehicle control unit.

35. (New) The method as recited in Claim 34, further comprising:  
causing the external host to poll the USB endpoints according to a priority sequence.